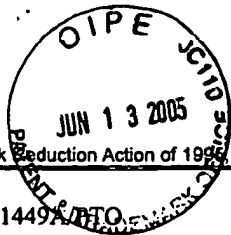


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<sup>1</sup>Unique citation designation number. <sup>2</sup>See attached Kinds of U.S. Patent Documents. <sup>3</sup>Enter Office that issued the document by the two-letter code (WIPO Standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. <sup>6</sup>Applicant is to place a check mark here if English language Translation is attached.



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<b>Substitute for form 1449A/PTO</b>  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (Use as many sheets as necessary)				Application Number		10/643,750		
				Filing Date		8/18/2003		
				First Named Inventor:		Wayne M. Struble		
				Examiner: E. Urban		GROUP: 2133		
Sheet	1	of	1	Attorney Docket Number		TRQ-00002		
<b>U.S. PATENT DOCUMENTS</b>								
Examiner Initials*	Cited No. <sup>1</sup>	Document Number Number - Kind Code <sup>2</sup> (if known)		Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Passages or Figures Appear	
nVo	B01	US-5,047,674		09-10-1991	Clark et al.			
nVo	B02	US-4,987,392		01-22-1991	Clark et al.			
nVo	B03	US-4,639,614		01-27-1987	Basile			
nVo	B04	US-2003/0090313		05-15-2003	Burgener et al.			
<b>FOREIGN PATENT DOCUMENTS</b>								
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document Cntry Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)			Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Figures Appear	T <sup>6</sup>
<b>OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)</b>								
nVo	Numata et al. "A high-power-handling GSM switch IC with new adaptive-control-voltage-generator circuit scheme," 2003 IEEE Radio Frequency Integrated Circuits (RFIC) Symposium, Digest of Papers, Philadelphia, PA, June 8-10 2003, pp. 233-236							
nVo	Miyatsuji et al. "A GaAs High-Power RF Single-Pole Double-Throw Switch IC for Digital Mobile Communication System," IEEE International Solid State Circuits Conference, New York, Vol. 37, 1 February 1994, pp. 34-35, 305							
nVo	Jones, S. "Reduce Insertion Loss in GaAs FET RF Switch Using Negative Voltage Derived from RF Output," Motorola Technical Developments, Motorola, Inc., Schaumburg, IL, Vol. 41, May 2000, pp. 205-206							
EXAMINER SIGNATURE: <i>nguyen</i>					DATE CONSIDERED: 1-8-2006			

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